

ABSTRACT OF THE DISCLOSURE

A signal line and a pixel capacitor wire that doubles as a pixel capacitor electrode are fabricated parallel to each other from the same electrode layer through patterning thereof. Therefore, no additional steps are required to form the pixel capacitor wire. In such an arrangement, the pixel capacitor wire and the signal line are disposed parallel to each other; therefore, delays of signal transmission in the signal line and crosstalk between pixels are prevented from occurring. The active matrix substrate incorporating this arrangement is suitably used in liquid crystal display devices, image sensors, and the like. Similar advantages are available with an arrangement in which the signal line and the pixel capacitor wire are disposed parallel to each other, and the storage capacitor electrode, which will constitute a storage capacitor with the pixel electrode therebetween, and the scanning line are fabricated from the same electrode layer through patterning thereof.